



DT1240A-04LP

Product Summary

V _{BR} (Min)	IPP (Max)	С _Т (Тур)
5V	5.5A	0.55pF

Description

The DT1240A-04LP is a high-performance device suitable for protecting four high-speed I/Os. These devices are assembled in U-DFN2510-10 packages and have high ESD surge capability and low capacitance.

Applications

Typically used at high-speed ports such as USB2.0, USB3.0, USB3.1, IEEE1394 (Firewire[®], iLink), Serial ATA, DVITM, HDMI1.4TM, HDMI2.0TM and PCI.

4 CHANNEL LOW CAPACITANCE TVS DIODE ARRAY

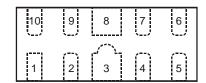
Features

- Clamping Voltage: 7.5V at 10A 100ns, TLP 8.2V at 5.5A (8μs/20μs)
- IEC 61000-4-2 (ESD): Air ±16kV, Contact ±14kV
- IEC 61000-4-5 (Lighting): 5.5A (8/20µs)
- 4 Channels of ESD Protection
- Low Channel Input Capacitance of 0.55pF Typical
- TLP Dynamic Resistance: 0.2Ω
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

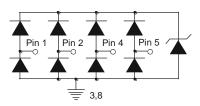
Mechanical Data

- Case: U-DFN2510-10
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Schematic
- Terminals: Finish NiPdAu, Solderable per MIL-STD-202, Method 208 @
- Weight: 0.038 grams (Approximate)

Pin #	Description
1, 2, 4, 5	I/O
6, 7, 9, 10	No Connection
3, 8	V _{SS}



Pin Description (Top View)



Device Schematic

Ordering Information (Note 4)

Product	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity per Reel
DT1240A-04LP-7	Standard	QE5	7	8	3,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information

QE5	YM
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QE5 = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

	Date Code Key												
	Year	20	16	20	17	20	18	20	19	20	20	20	21
	Code	[)	E			F	(G	I	Η		
Г	Month	.lan	Feb	Mar	Apr	May	Jun	Jul	Αιια	Sep	Oct	Nov	Dec

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	Ν	D

FireWire is a registered trademark of Apple Computer Inc. DT1240A-04LP Document number: DS38634 Rev. 4 – 2



Maximum Ratings ($@T_A = +25^{\circ}C$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Condition
Peak Pulse Current, per IEC 61000-4-5	IPP	5.5	А	I/O to V _{SS} , 8/20µs
Peak Pulse Power, per IEC 61000-4-5	P _{PP}	52	W	I/O to V _{SS} , 8/20µs
Operating Voltage (DC)	V _{DC}	3.6	V	I/O to V _{SS}
ESD Protection – Contact Discharge, per IEC 61000-4-2	Vesd_contact	±14	kV	I/O to V _{SS}
ESD Protection – Air Discharge, per IEC 61000-4-2	V _{ESD_AIR}	±16	kV	I/O to V _{SS}
Operating Temperature	T _{OP}	-55 to +85	°C	—
Storage Temperature	T _{STG}	-55 to +150	°C	—

Thermal Characteristics

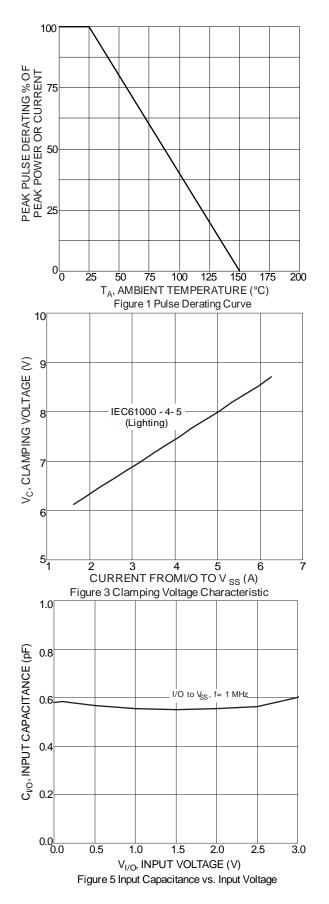
Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	PD	350	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	$R_{ extsf{ heta}JA}$	360	°C/W

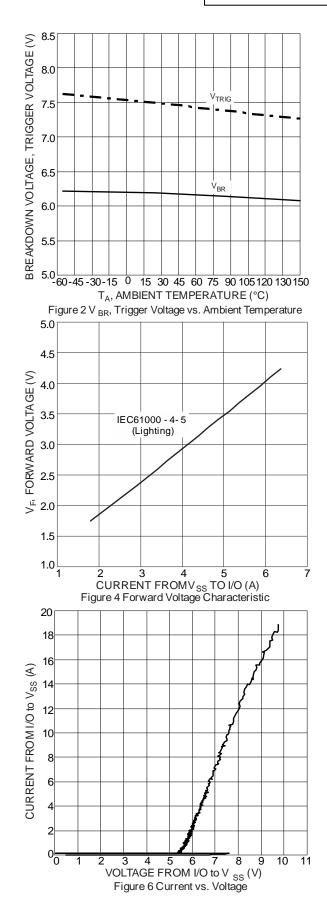
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Working Voltage	V _{RWM}	_		3.3	V	I _R =1mA, I/O to V _{SS}
Reverse Current	I _R	_	_	1.0	μA	$V_R = 3.3V$, I/O to V_{SS}
Reverse Breakdown Voltage	V _{BR}	5	-	—	V	$I_R = 1mA$, I/O to V_{SS}
Forward Clamping Voltage	VF	-1.0	-0.85	_	V	$I_F = -15 \text{mA}$, I/O to V_{SS}
Reverse Clamping Voltage (Note 6)	Vc	_	8.2	9.5	V	I _{PP} = 5.5A, I/O to V _{SS} , 8/20µs
ESD Clamping Voltage	V _{ESD}	_	7.5	_	V	TLP, 10A, t_P = 100ns, I/O to V _{SS}
Dynamic Reverse Resistance	R _{DIF-R}	_	0.2	_	Ω	TLP, 10A, t_P = 100ns, I/O to V _{SS}
Dynamic Forward Resistance	R _{DIF-F}	_	0.2	_	Ω	TLP, 10A, t_P = 100ns, V _{SS} to I/O
Channel Input Capacitance	CI/O	_	0.55	0.65	pF	V _{I/O} = 2.5V, V _{SS} = 0V, f = 1MHz
Delta C _{I/O}	CI/OMAX-CI/OMIN	_	0.04	_	pF	CI/OMAX-CI/OMIN

Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's website at http://www.diodes.com/package-outlines.html. 6. Clamping voltage value is based on an 8µs x20µs peak pulse current (Ipp) waveform.



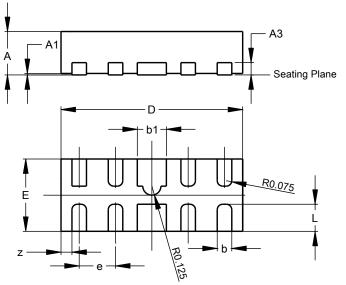






Package Outline Dimensions

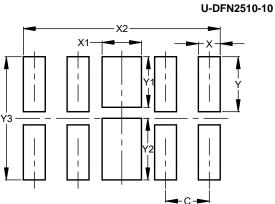
Please see http://www.diodes.com/package-outlines.html for the latest version.



ι	U-DFN2510-10							
Dim	Min	Max	Тур					
Α	0.545	0.605	0.575					
A1	0.00	0.05	0.03					
A3	١	-	0.13					
b	0.15	0.25	0.20					
b1	0.35	0.45	0.40					
D	2.450	2.575	2.500					
е	-	-	0.50					
E	0.950	1.075	1.000					
L	0.325	0.425	0.375					
z	-	-	0.150					
All D	imensi	ons in	mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
С	0.500
Х	0.250
X1	0.450
X2	2.250
Y	0.625
Y1	0.575
Y2	0.700
Y3	1.400

U-DFN2510-10



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